UPC Developers Workshop
Marvin Center
The George Washington University
Washington D.C.
September 22-23, 2008

Agenda

Monday September 22 (Room 308)

8:30-9:30
Registration and Continental Breakfast

9:30-10:30
Session 1: Opening session
Welcome and Introductions (Tarek El-Ghazawi)
Project Status (Mike Lewis)
Quick Research and Development Reports (All)
Workshop Objectives and Plans (Bill Carlson)

10:30-12:00
Session 2: Collective Operations [Steve Seidel, Moderator]
Report on the PGAS Collectives Workshop (George Almasi)
Autotuning of UPC Collectives (Rajesh Nishtala)
Discussion

12:00-1:30
Working Lunch [PGAS BoF and Exhibits at SC08]

1:30-2:30
Session 3: Parallel I/O [Brian Wibecan, Moderator]
Reference Implementations and UPC-I/O Specs (Samy Al-Bahra/Abdullah Kayi)
Discussion

2:30-4:30
Session 4: UPC for Multicore and Exotic Processors [Flash Gordon, Moderator]
UPC on FPGAs and Cell (Olivier Serres / Tarek El-Ghazawi)
Scheduling UPC Threads on Multicore and Cell (Kathy Yelick)
Discussion

6:00 Workshop Dinner @ Neyla Restaurant – Georgetown (www.neyla.com)
Tuesday, September 23 (Room 405)

8:30-9:00
Continental Breakfast

9:00-10:30
Session 5: Tools [Michael Lewis, Moderator]
UF Performance Toolset (Hung-Hsun Su and Max Billingsley)
Discussion

10:30-12:00
Session 6: Misc. (Interoperability, Libraries, Testing, ) [George Almasi, Moderator]
A UPC Performance Model (Steve Seidel)
GUTS (Abdullah Kayi)
Discussion

12:00-2:00
Working Lunch: Next Specs and General Direction [Bill Carlson and Michael Lewis]

2:00 Adjourn

Coffee and/or refreshments scheduled at 10:30 and 2:30
No formal breaks are scheduled to allow a single threaded program and adequate finishing times.